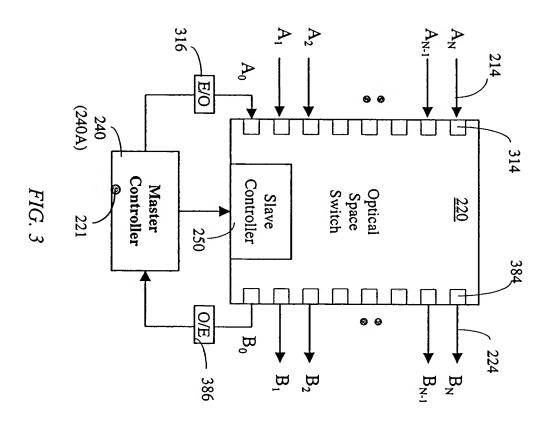


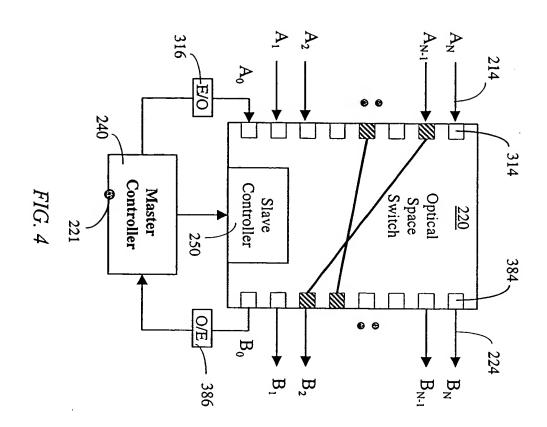
FIG. 2





)





Channel switched

Burst switches



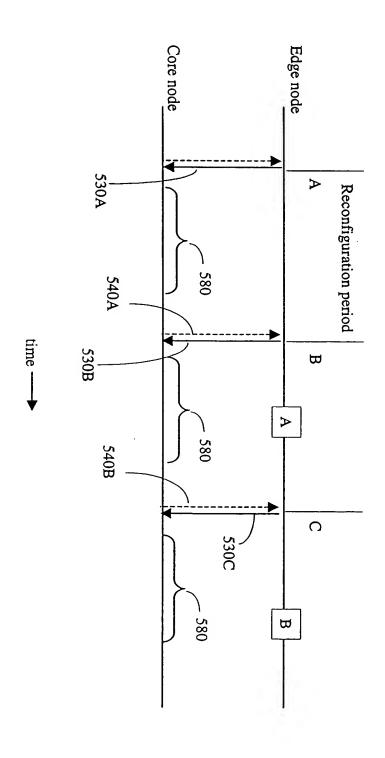
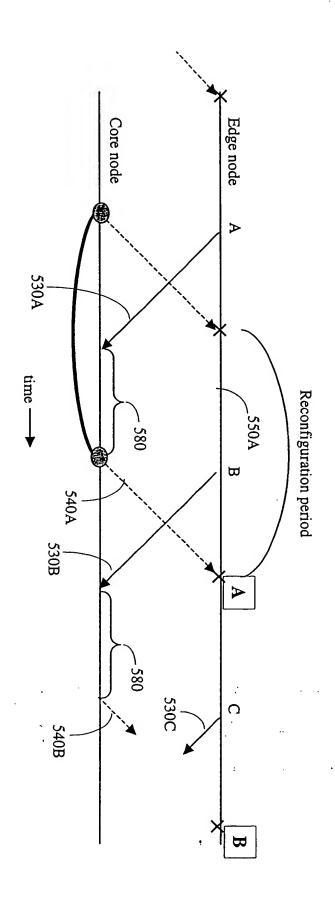
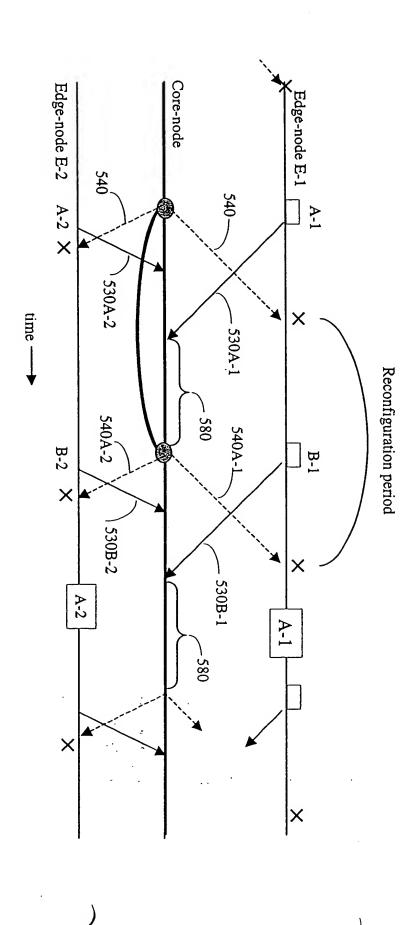


FIG. 5

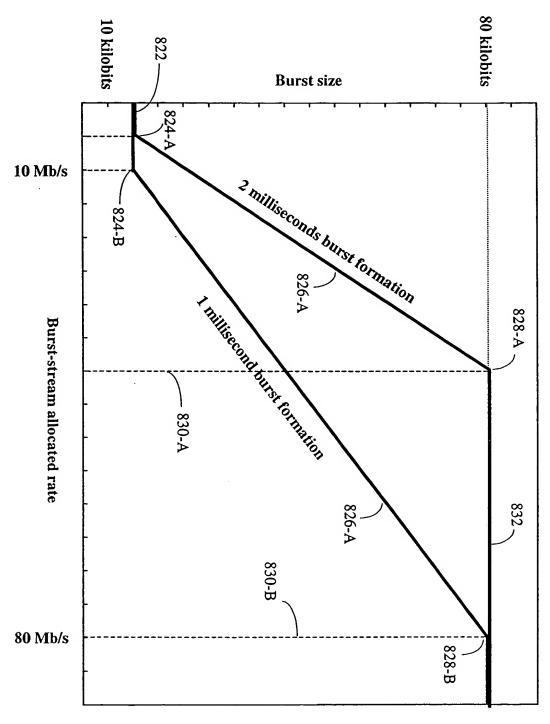




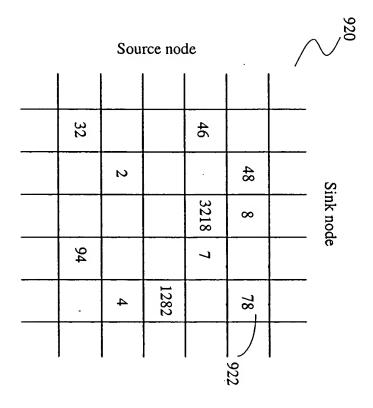


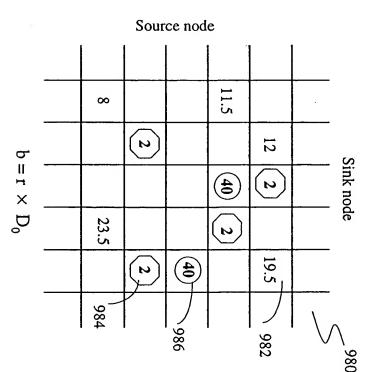




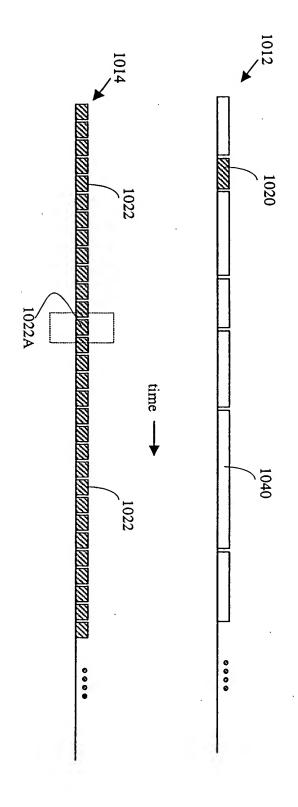














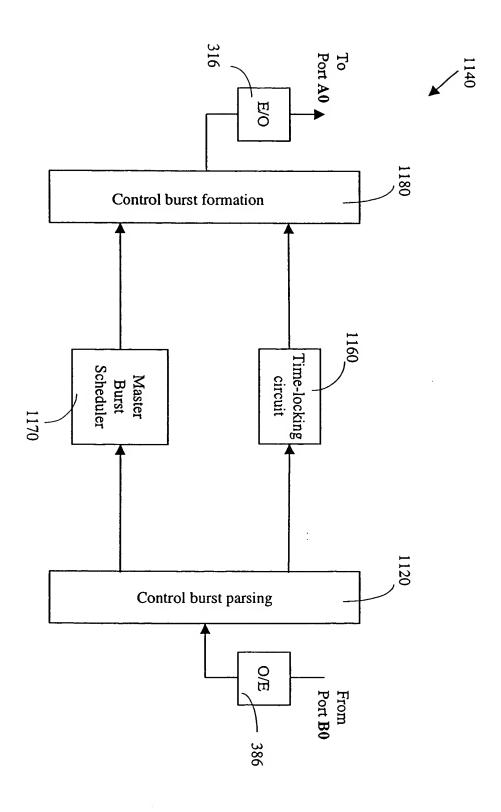
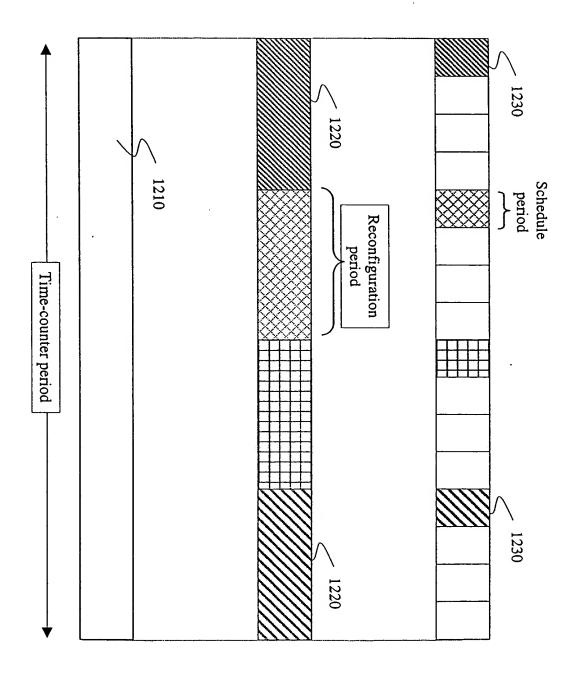


FIG. 1.

)







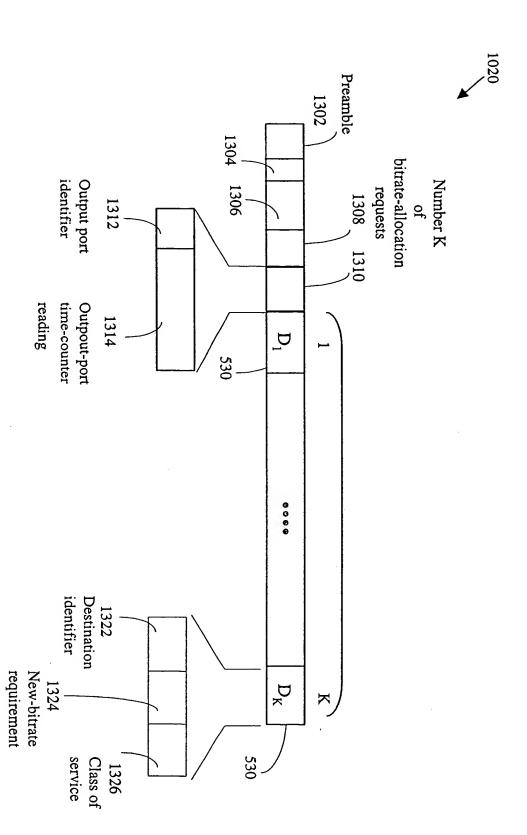
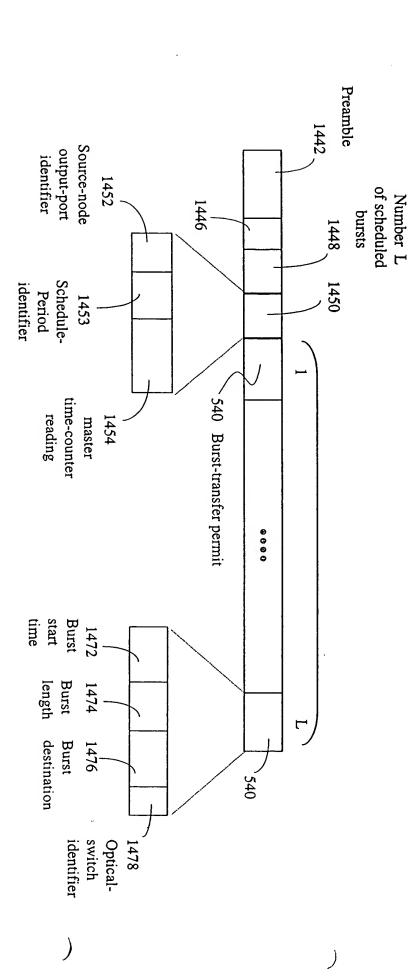
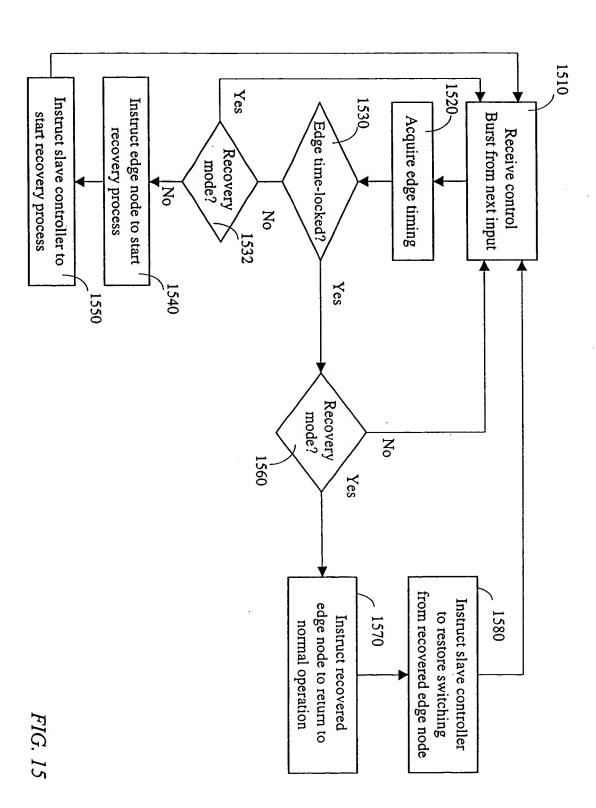


FIG. 13









)



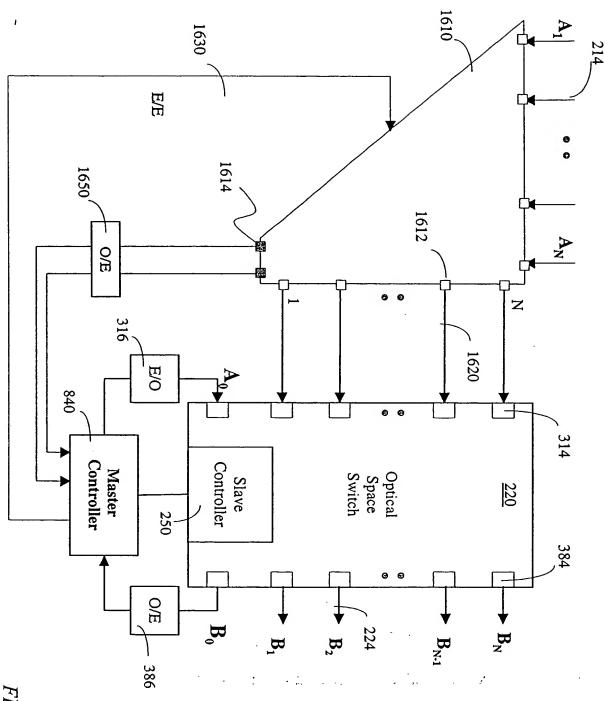
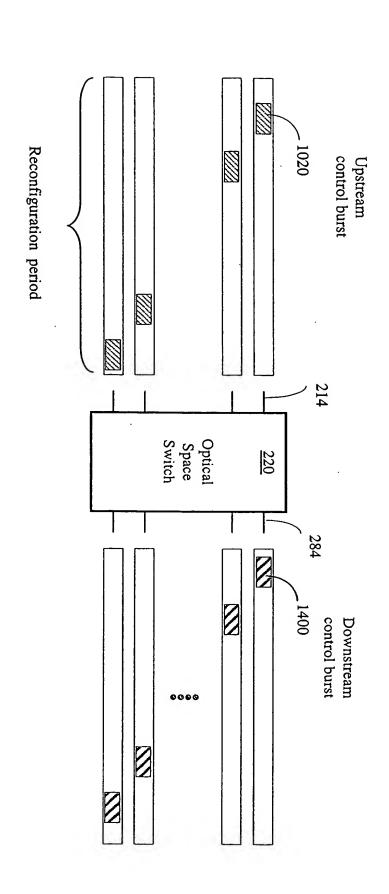


FIG. 16

) .







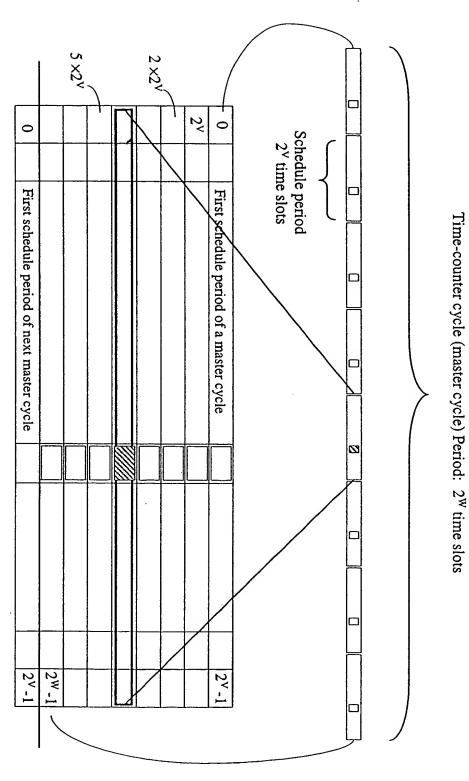
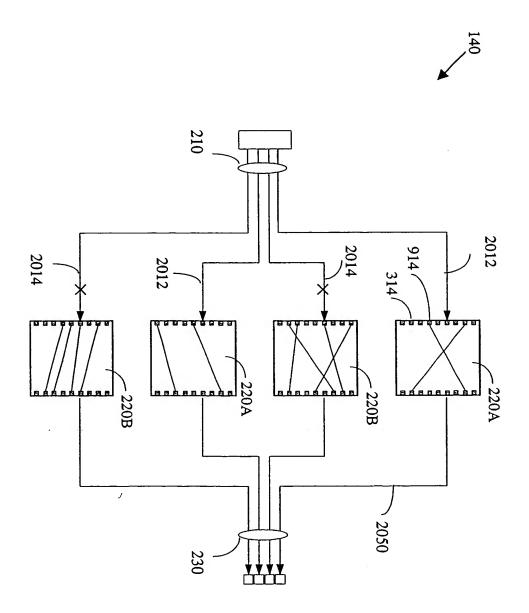
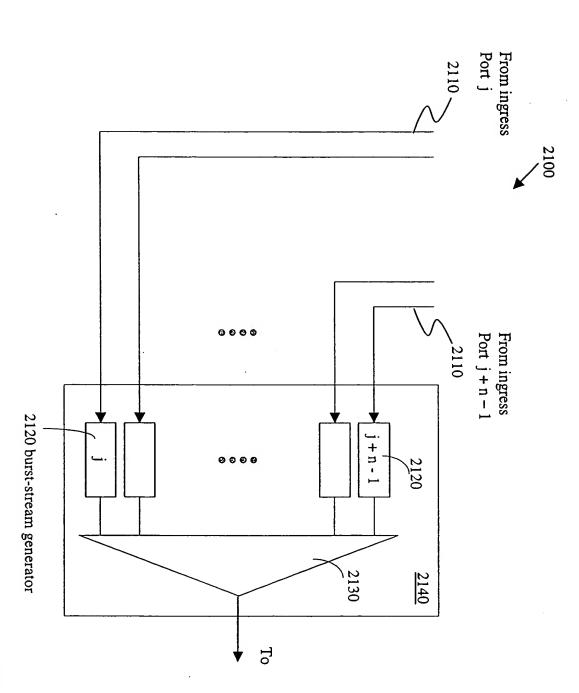


FIG. 19









j

FIG. 21



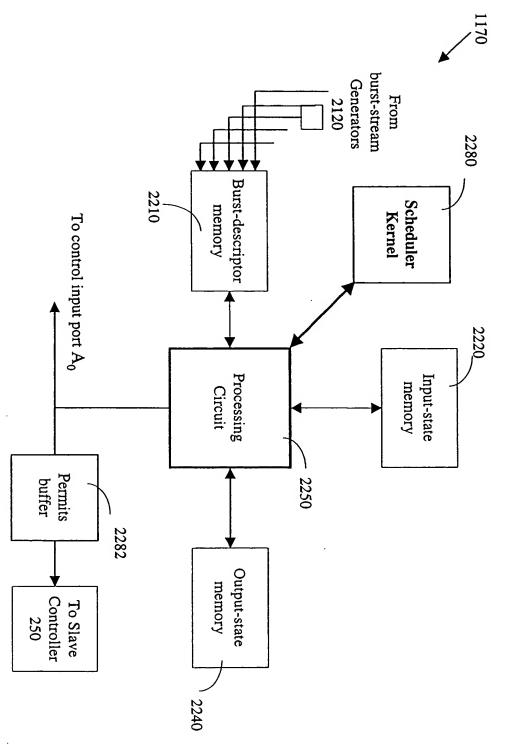
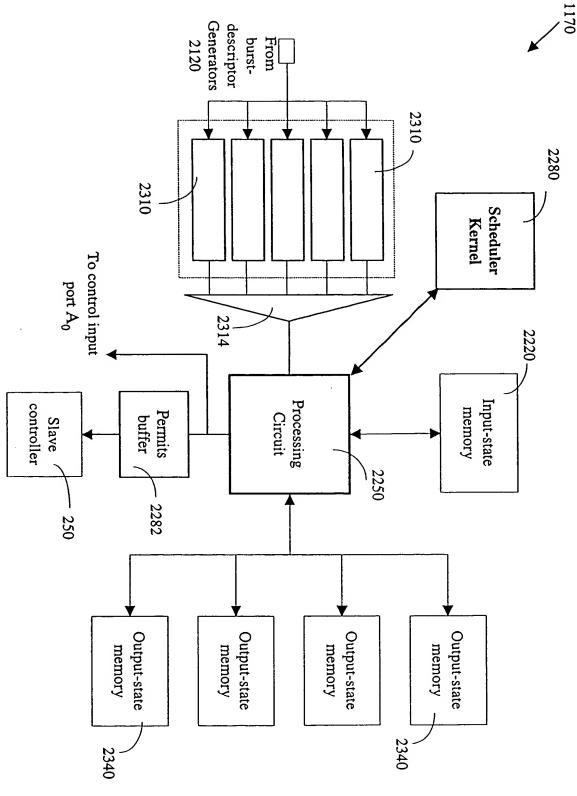
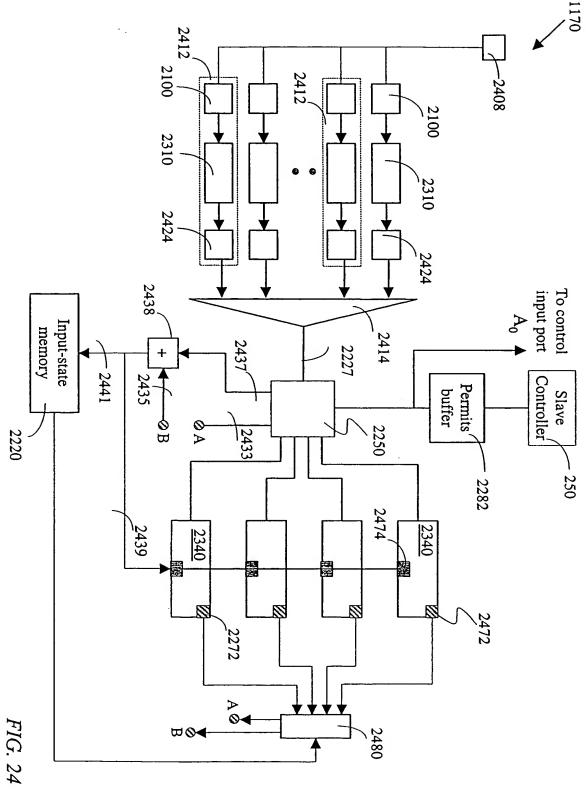


FIG. 22

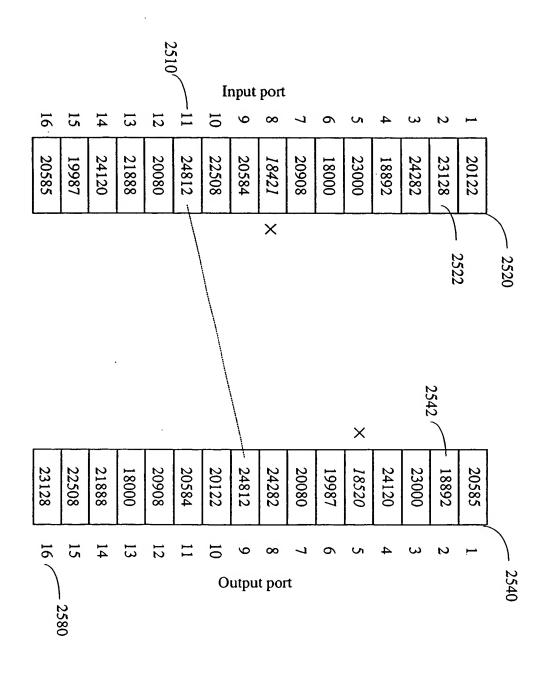














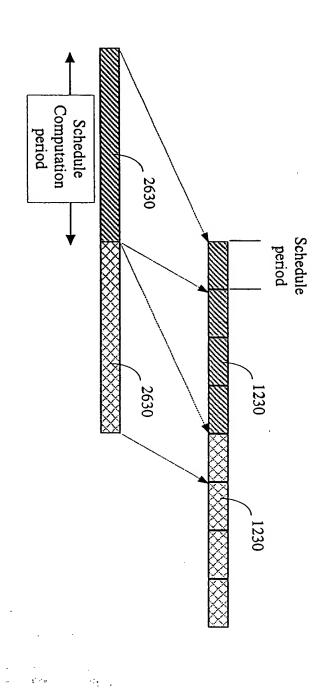
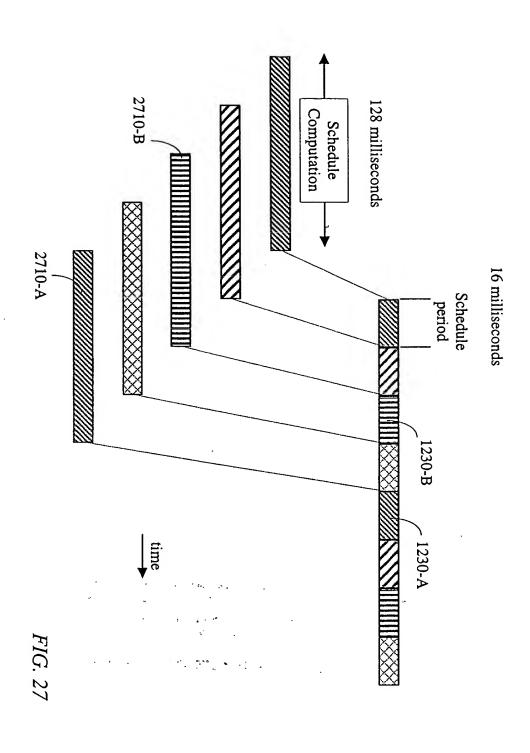
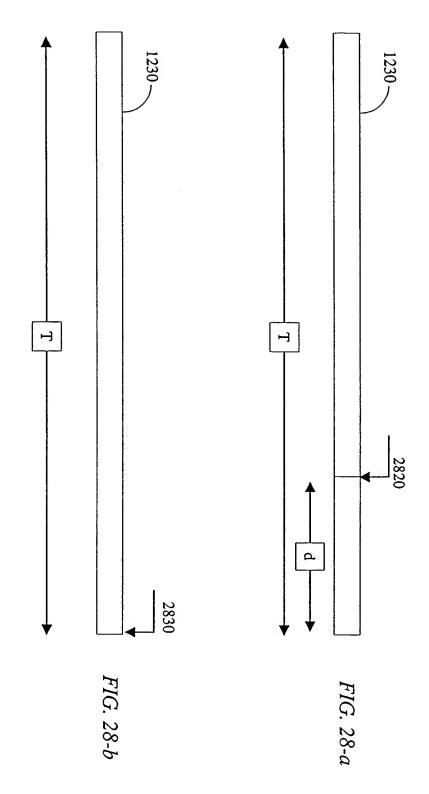


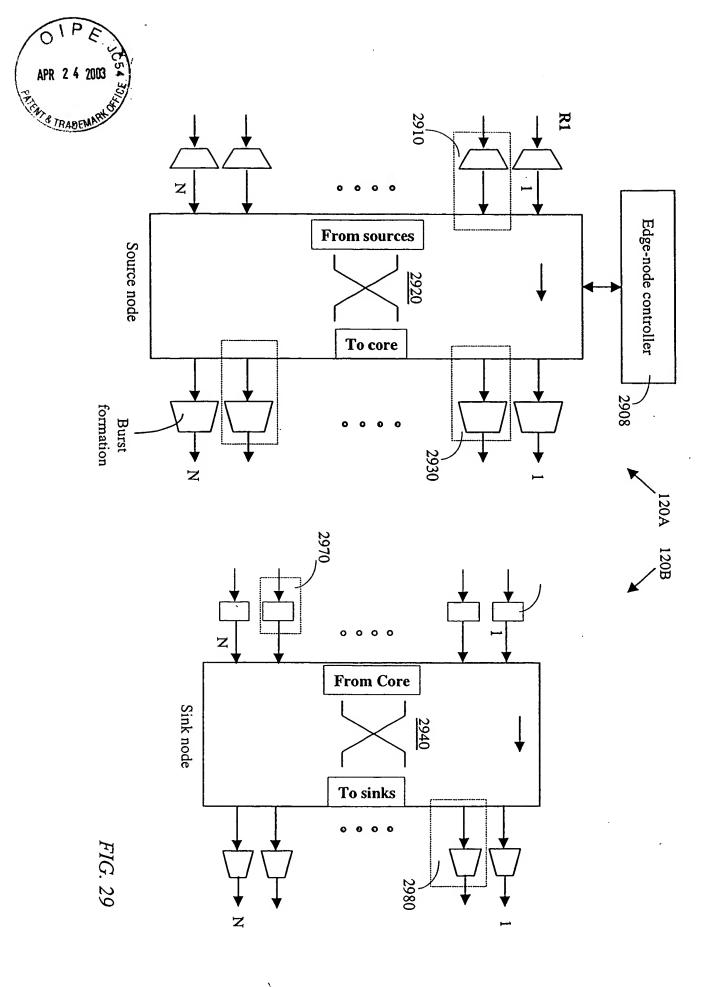
FIG. 26



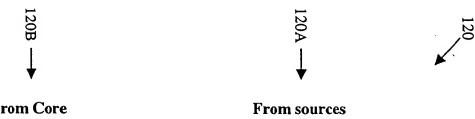




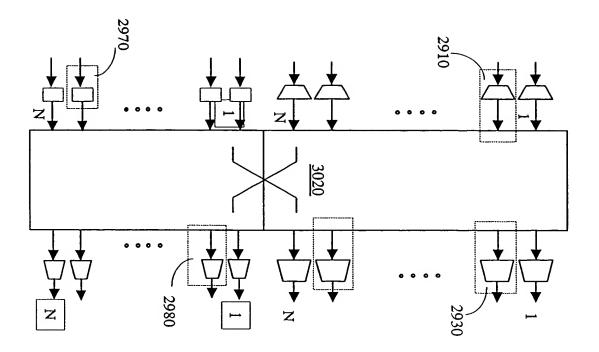








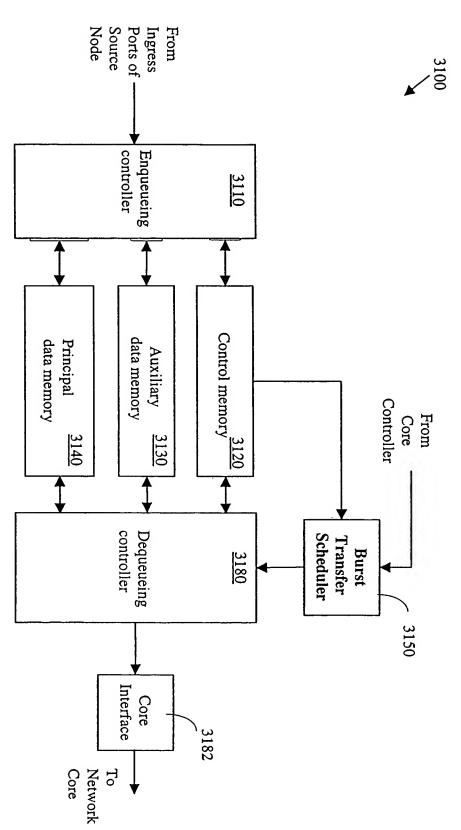
From Core



To sinks

To core







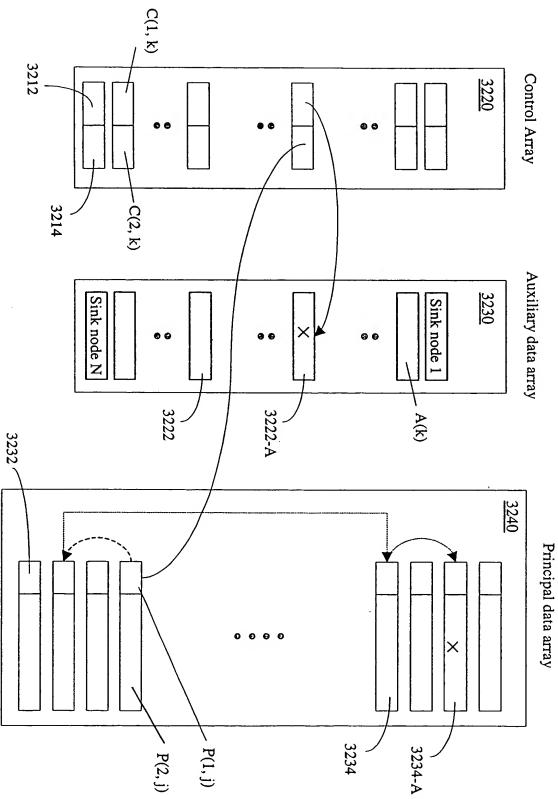


FIG. 32



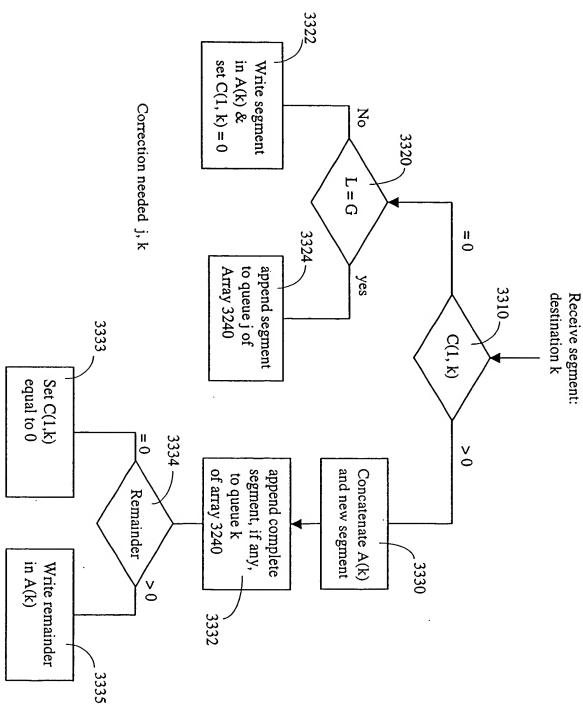


FIG. 33



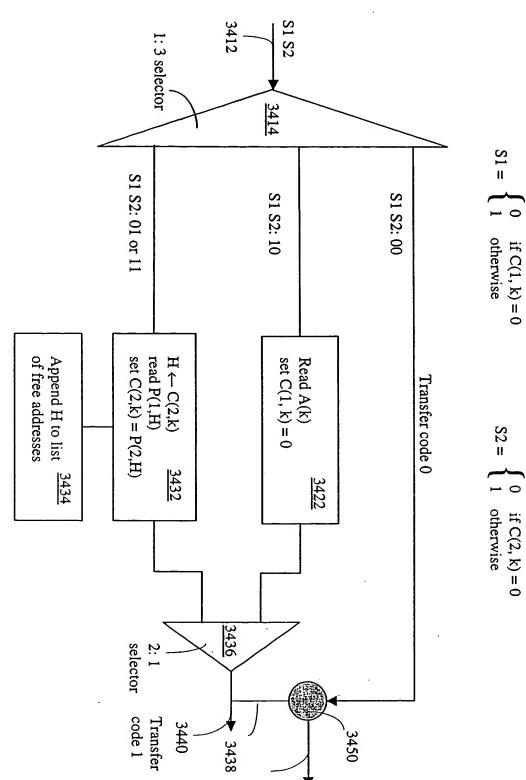


FIG. 34